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TRANSMITTAL FORM (to be used for all correspondence during pendency of filed application)		Application Number	10/619,738	
		Filing Date	July 14, 2003	
		First Named Inventor	Sabhapathi Naidu Annamaneni	
		Group Art Unit Number	2812	
		Examiner Name	Not yet assigned	
Total Number of Pages in This Submission	2	Attorney Docket Number	22272-0803	30
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Copies of IDS Cited References		After Allowance Communication to Group		
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Attorney/Reg. No.: Michael W. Farn, Reg. No. 41,015			Dated:	JAN- 12, 2006
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IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

Sabhapathi Naidu Annamaneni and Sarangkumar Sharadchandra

Bhavsar

APPLICATION NO.:

10/619,738

FILING DATE:

July 14, 2003

TITLE:

System and Method for Automating Integration of Semiconductor

Work in Process Updates

EXAMINER:

Not yet assigned

GROUP ART UNIT:

2812

ATTY. DKT. NO.:

22272-08030

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Michael W. Farn, Reg. No. 41,015

COMMISSIONER FOR PATENTS P.O. BOX 1450 **ALEXANDRIA, VA 22313-1450**

STATUS REQUEST

SIR:

Our file for the subject application reveals that there has been no action on this application since the filing of our Information Disclosure Statement on January 20, 2005. We submitted a Status Request on July 19, 2005, but never received a reply.

Please inform the undersigned, at the below stated address, of the status of this application.

Respectfully submitted,

Dated:

7HLP-, 2006

Michael W. Farn, Reg. No. 41.015

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